

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	4964	(step OR stepped) ADJ4 (simulation OR simulate)	US-PGPUB; USPAT	OR	ON	2006/11/14 18:13
L2	1318	(step OR stepped) ADJ4 (simulation OR simulate) AND "703".CLAS.	US-PGPUB; USPAT	OR	ON	2006/11/14 18:13
L3	124	(step OR stepped) ADJ4 (simulation OR simulate) AND "703".CLAS. AND (object NEAR5 oriented)	US-PGPUB; USPAT	OR	ON	2006/11/14 18:14
L4	112	(step OR stepped) ADJ4 (simulation OR simulate) AND "703".CLAS. AND (object NEAR5 oriented) AND (scheduler OR trigger OR control)	US-PGPUB; USPAT	OR	ON	2006/11/14 18:15
L5	99	(step OR stepped) ADJ4 (simulation OR simulate) AND "703".CLAS. AND (object NEAR5 oriented) AND (scheduler OR trigger OR control) AND (circuit OR digital OR logic OR electronic)	US-PGPUB; USPAT	OR	ON	2006/11/14 18:15
L6	4	(step OR stepped) ADJ4 (simulation OR simulate) AND "703/16".CCLS. AND (object NEAR5 oriented) AND (scheduler OR trigger OR control) AND (circuit OR digital OR logic OR electronic)	US-PGPUB; USPAT	OR	ON	2006/11/14 18:16
L7	11	(step OR stepped) ADJ4 (simulation OR simulate) AND ("703/16".CCLS. OR 703/17.CCLS.) AND (object NEAR5 oriented) AND (scheduler OR trigger OR control) AND (circuit OR digital OR logic OR electronic)	US-PGPUB; USPAT	OR	ON	2006/11/14 18:16

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	35759	object ADJ oriented	US-PGPUB; USPAT	OR	ON	2006/11/14 16:34
L2	6050	(object ADJ oriented) and (simulation OR simulate)	US-PGPUB; USPAT	OR	ON	2006/11/14 16:34
L3	4514	(object ADJ oriented) AND (simulation OR simulate) AND (logic OR circuit)	US-PGPUB; USPAT	OR	ON	2006/11/14 16:35
L4	2208	(object ADJ oriented) AND (simulation OR simulate) AND (logic OR circuit) AND (trigger OR controller OR master)	US-PGPUB; USPAT	OR	ON	2006/11/14 16:36
L5	340	(object ADJ oriented) AND (event WITH (simulation OR simulate)) AND (logic OR circuit) AND (trigger OR controller OR master)	US-PGPUB; USPAT	OR	ON	2006/11/14 16:36
L6	340	(object ADJ oriented) AND (event WITH (simulation OR simulate)) AND (logic OR circuit) AND (trigger OR controller OR master OR scheduler)	US-PGPUB; USPAT	OR	ON	2006/11/14 16:37
L7	136	(object ADJ oriented) AND (event WITH (simulation OR simulate)) AND (logic OR circuit) AND (trigger OR controller OR master OR scheduler) AND ((system ADJ4 level) OR heirarchy)	US-PGPUB; USPAT	OR	ON	2006/11/14 16:38
L8	30	(object ADJ oriented) AND (event WITH (simulation OR simulate)) AND (logic OR circuit) AND (trigger OR controller OR master OR scheduler) AND ((system ADJ4 level) OR heirarchy) AND "703". CLAS.	US-PGPUB; USPAT	OR	ON	2006/11/14 16:41
L9	4	(object ADJ oriented) AND (event WITH (simulation OR simulate)) AND (logic OR circuit) AND (trigger OR controller OR master OR scheduler) AND ((system ADJ4 level) OR heirarchy) AND "703/16". CCLS.	US-PGPUB; USPAT	OR	ON	2006/11/14 16:42